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\*/

#ifndef \_DREQ\_H

#define \_DREQ\_H

/\*\*

\* \file rp2040/dreq.h

\*/

#ifdef \_\_ASSEMBLER\_\_

#define DREQ\_PIO0\_TX0 0

#define DREQ\_PIO0\_TX1 1

#define DREQ\_PIO0\_TX2 2

#define DREQ\_PIO0\_TX3 3

#define DREQ\_PIO0\_RX0 4

#define DREQ\_PIO0\_RX1 5

#define DREQ\_PIO0\_RX2 6

#define DREQ\_PIO0\_RX3 7

#define DREQ\_PIO1\_TX0 8

#define DREQ\_PIO1\_TX1 9

#define DREQ\_PIO1\_TX2 10

#define DREQ\_PIO1\_TX3 11

#define DREQ\_PIO1\_RX0 12

#define DREQ\_PIO1\_RX1 13

#define DREQ\_PIO1\_RX2 14

#define DREQ\_PIO1\_RX3 15

#define DREQ\_SPI0\_TX 16

#define DREQ\_SPI0\_RX 17

#define DREQ\_SPI1\_TX 18

#define DREQ\_SPI1\_RX 19

#define DREQ\_UART0\_TX 20

#define DREQ\_UART0\_RX 21

#define DREQ\_UART1\_TX 22

#define DREQ\_UART1\_RX 23

#define DREQ\_PWM\_WRAP0 24

#define DREQ\_PWM\_WRAP1 25

#define DREQ\_PWM\_WRAP2 26

#define DREQ\_PWM\_WRAP3 27

#define DREQ\_PWM\_WRAP4 28

#define DREQ\_PWM\_WRAP5 29

#define DREQ\_PWM\_WRAP6 30

#define DREQ\_PWM\_WRAP7 31

#define DREQ\_I2C0\_TX 32

#define DREQ\_I2C0\_RX 33

#define DREQ\_I2C1\_TX 34

#define DREQ\_I2C1\_RX 35

#define DREQ\_ADC 36

#define DREQ\_XIP\_STREAM 37

#define DREQ\_XIP\_SSITX 38

#define DREQ\_XIP\_SSIRX 39

#define DREQ\_DMA\_TIMER0 59

#define DREQ\_DMA\_TIMER1 60

#define DREQ\_DMA\_TIMER2 61

#define DREQ\_DMA\_TIMER3 62

#define DREQ\_FORCE 63

#else

/\*\*

\* \brief DREQ numbers for DMA pacing on RP2040 (used as typedef \ref dreq\_num\_t)

\* \ingroup hardware\_dma

\*/

typedef enum dreq\_num\_rp2040 {

DREQ\_PIO0\_TX0 = 0, ///< Select PIO0's TX FIFO 0 as DREQ

DREQ\_PIO0\_TX1 = 1, ///< Select PIO0's TX FIFO 1 as DREQ

DREQ\_PIO0\_TX2 = 2, ///< Select PIO0's TX FIFO 2 as DREQ

DREQ\_PIO0\_TX3 = 3, ///< Select PIO0's TX FIFO 3 as DREQ

DREQ\_PIO0\_RX0 = 4, ///< Select PIO0's RX FIFO 0 as DREQ

DREQ\_PIO0\_RX1 = 5, ///< Select PIO0's RX FIFO 1 as DREQ

DREQ\_PIO0\_RX2 = 6, ///< Select PIO0's RX FIFO 2 as DREQ

DREQ\_PIO0\_RX3 = 7, ///< Select PIO0's RX FIFO 3 as DREQ

DREQ\_PIO1\_TX0 = 8, ///< Select PIO1's TX FIFO 0 as DREQ

DREQ\_PIO1\_TX1 = 9, ///< Select PIO1's TX FIFO 1 as DREQ

DREQ\_PIO1\_TX2 = 10, ///< Select PIO1's TX FIFO 2 as DREQ

DREQ\_PIO1\_TX3 = 11, ///< Select PIO1's TX FIFO 3 as DREQ

DREQ\_PIO1\_RX0 = 12, ///< Select PIO1's RX FIFO 0 as DREQ

DREQ\_PIO1\_RX1 = 13, ///< Select PIO1's RX FIFO 1 as DREQ

DREQ\_PIO1\_RX2 = 14, ///< Select PIO1's RX FIFO 2 as DREQ

DREQ\_PIO1\_RX3 = 15, ///< Select PIO1's RX FIFO 3 as DREQ

DREQ\_SPI0\_TX = 16, ///< Select SPI0's TX FIFO as DREQ

DREQ\_SPI0\_RX = 17, ///< Select SPI0's RX FIFO as DREQ

DREQ\_SPI1\_TX = 18, ///< Select SPI1's TX FIFO as DREQ

DREQ\_SPI1\_RX = 19, ///< Select SPI1's RX FIFO as DREQ

DREQ\_UART0\_TX = 20, ///< Select UART0's TX FIFO as DREQ

DREQ\_UART0\_RX = 21, ///< Select UART0's RX FIFO as DREQ

DREQ\_UART1\_TX = 22, ///< Select UART1's TX FIFO as DREQ

DREQ\_UART1\_RX = 23, ///< Select UART1's RX FIFO as DREQ

DREQ\_PWM\_WRAP0 = 24, ///< Select PWM Counter 0's Wrap Value as DREQ

DREQ\_PWM\_WRAP1 = 25, ///< Select PWM Counter 1's Wrap Value as DREQ

DREQ\_PWM\_WRAP2 = 26, ///< Select PWM Counter 2's Wrap Value as DREQ

DREQ\_PWM\_WRAP3 = 27, ///< Select PWM Counter 3's Wrap Value as DREQ

DREQ\_PWM\_WRAP4 = 28, ///< Select PWM Counter 4's Wrap Value as DREQ

DREQ\_PWM\_WRAP5 = 29, ///< Select PWM Counter 5's Wrap Value as DREQ

DREQ\_PWM\_WRAP6 = 30, ///< Select PWM Counter 6's Wrap Value as DREQ

DREQ\_PWM\_WRAP7 = 31, ///< Select PWM Counter 7's Wrap Value as DREQ

DREQ\_I2C0\_TX = 32, ///< Select I2C0's TX FIFO as DREQ

DREQ\_I2C0\_RX = 33, ///< Select I2C0's RX FIFO as DREQ

DREQ\_I2C1\_TX = 34, ///< Select I2C1's TX FIFO as DREQ

DREQ\_I2C1\_RX = 35, ///< Select I2C1's RX FIFO as DREQ

DREQ\_ADC = 36, ///< Select the ADC as DREQ

DREQ\_XIP\_STREAM = 37, ///< Select the XIP Streaming FIFO as DREQ

DREQ\_XIP\_SSITX = 38, ///< Select the XIP SSI TX FIFO as DREQ

DREQ\_XIP\_SSIRX = 39, ///< Select the XIP SSI RX FIFO as DREQ

DREQ\_DMA\_TIMER0 = 59, ///< Select DMA\_TIMER0 as DREQ

DREQ\_DMA\_TIMER1 = 60, ///< Select DMA\_TIMER0 as DREQ

DREQ\_DMA\_TIMER2 = 61, ///< Select DMA\_TIMER1 as DREQ

DREQ\_DMA\_TIMER3 = 62, ///< Select DMA\_TIMER3 as DREQ

DREQ\_FORCE = 63, ///< Select FORCE as DREQ

DREQ\_COUNT

} dreq\_num\_t;

#endif

#endif // \_DREQ\_H